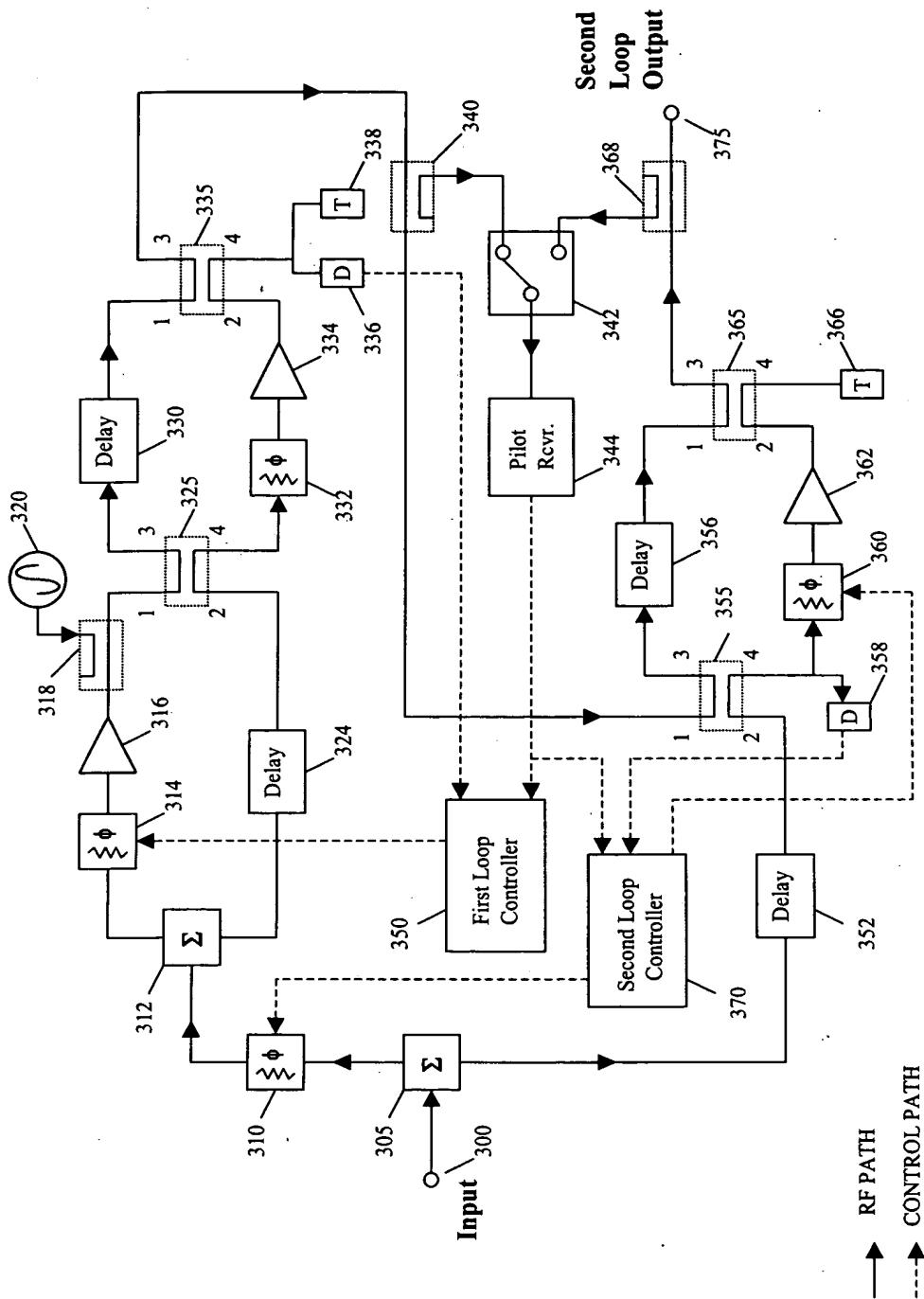


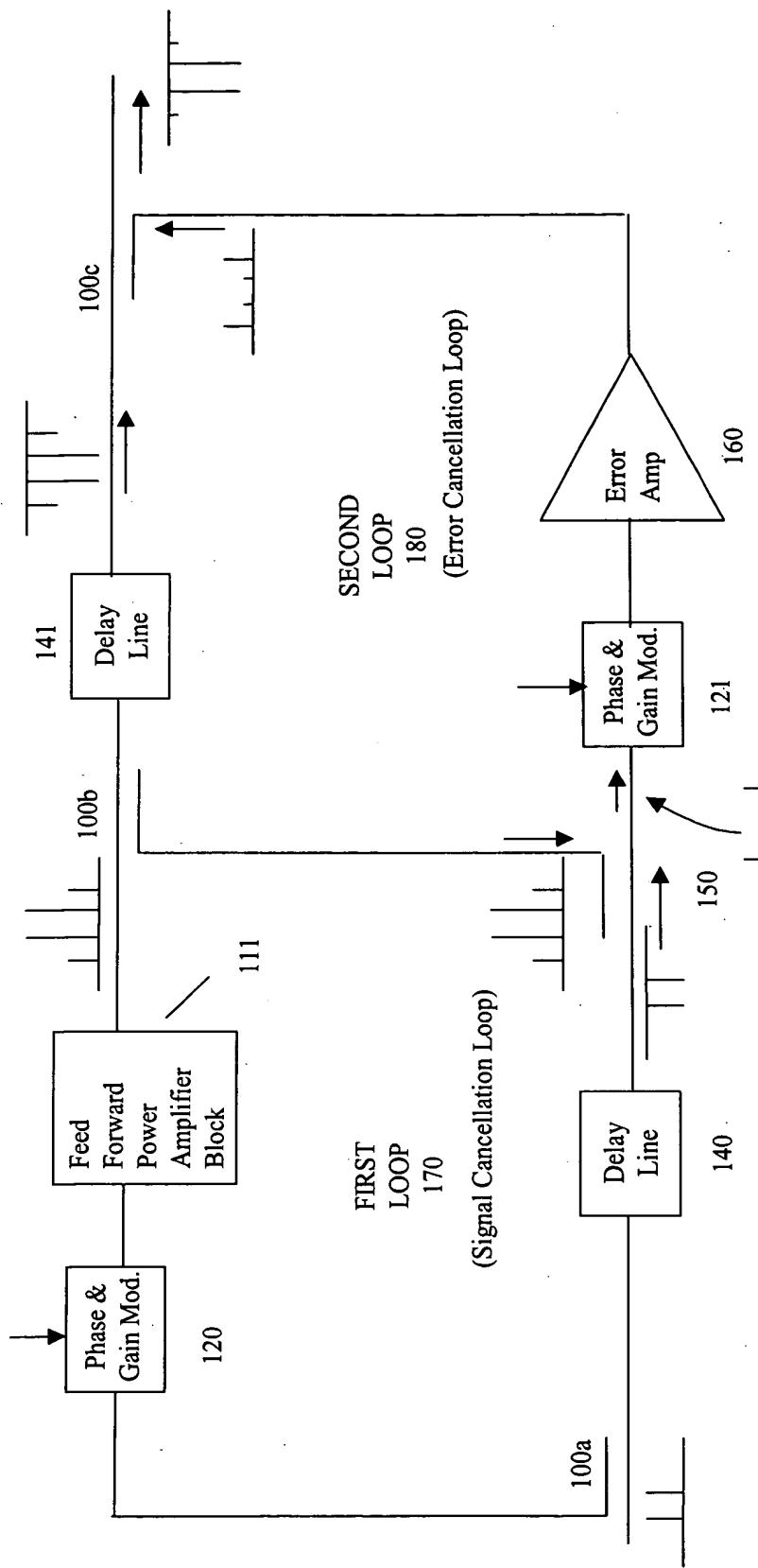
PRIOR ART

FIG. 1

FIG. 2

PRIOR ART

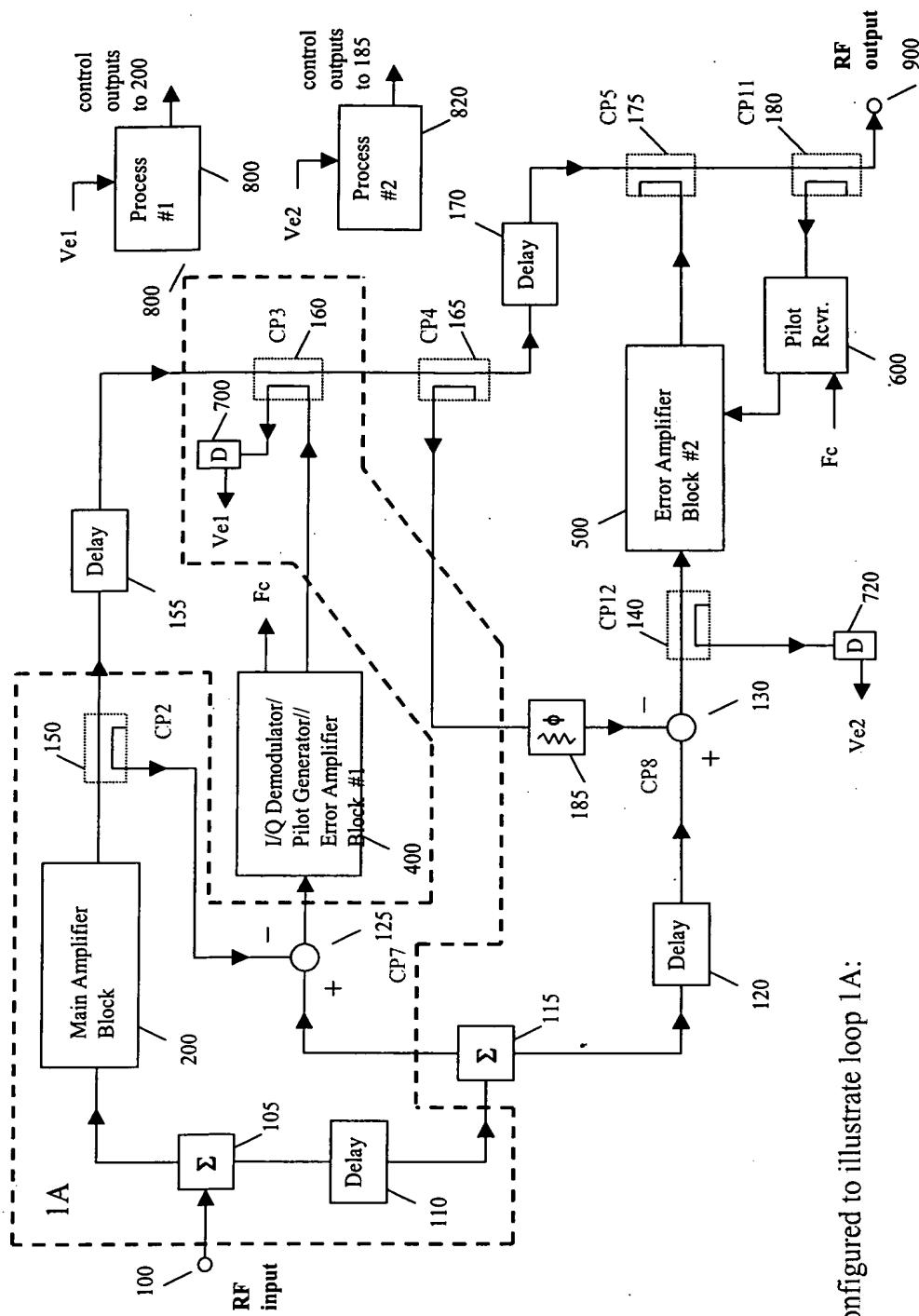


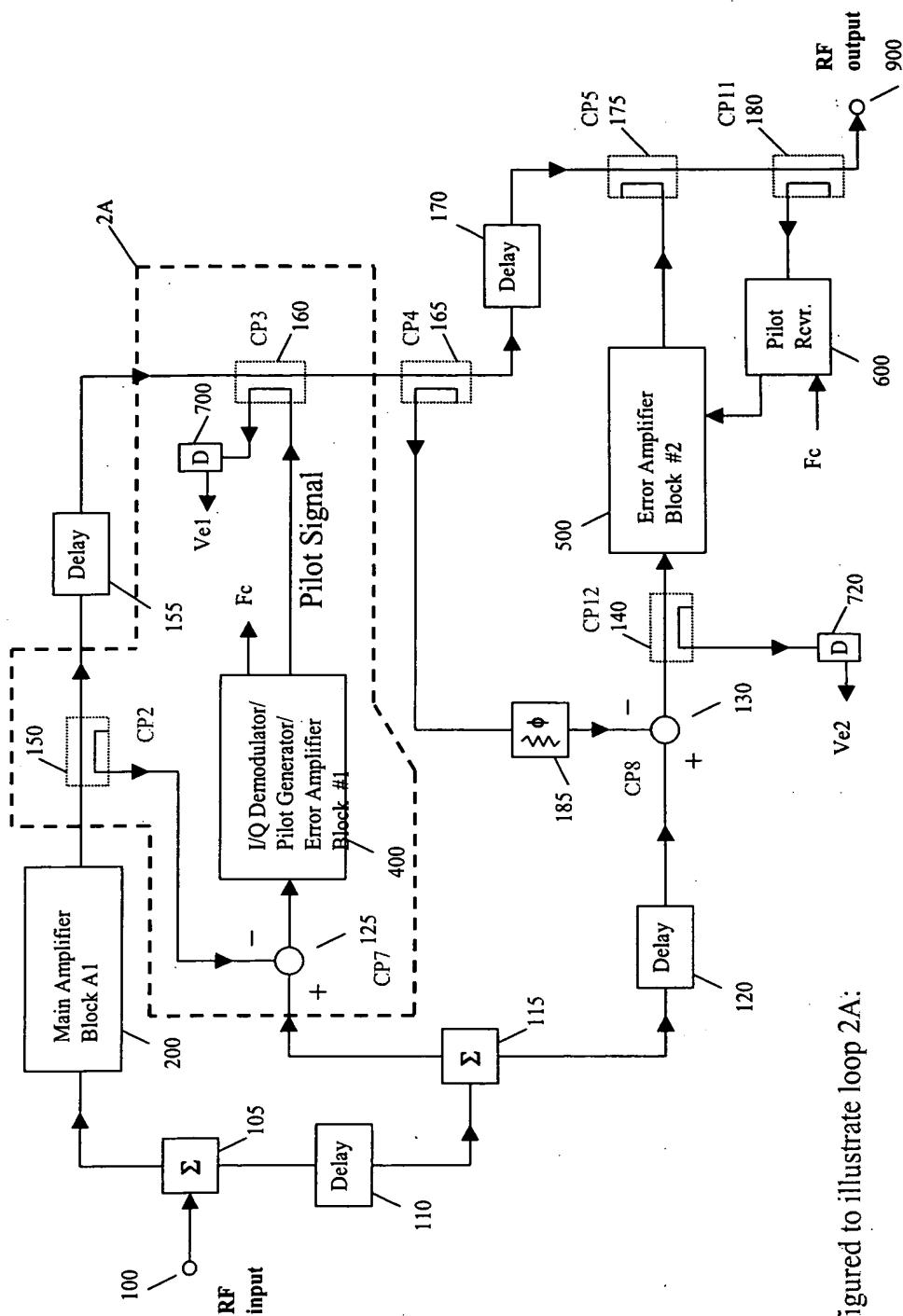


PRIOR ART

FIG. 3

FIG. 4

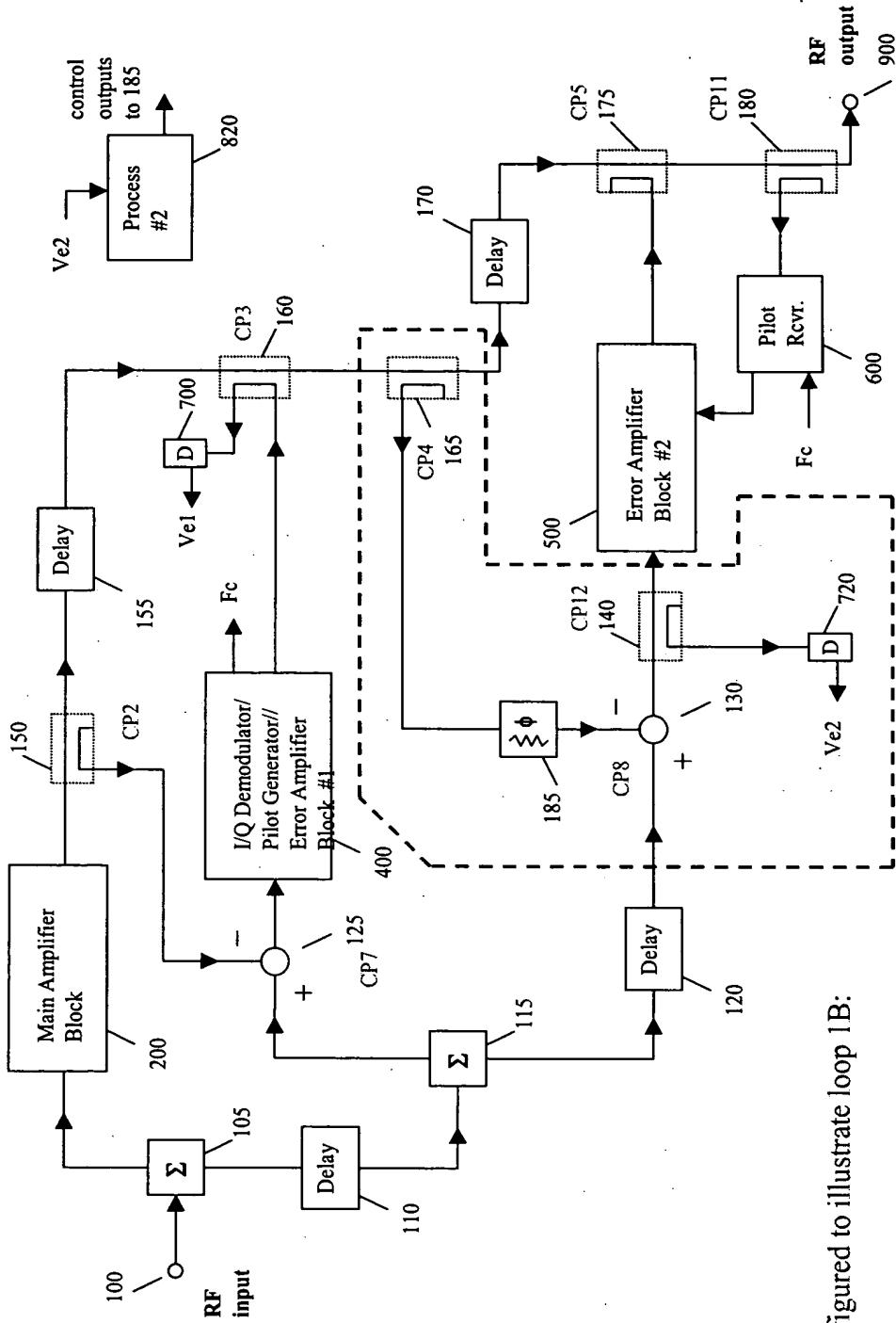




Configured to illustrate loop 2A:

FIG. 5

FIG. 6



configured to illustrate loop 1B:

FIG. 7

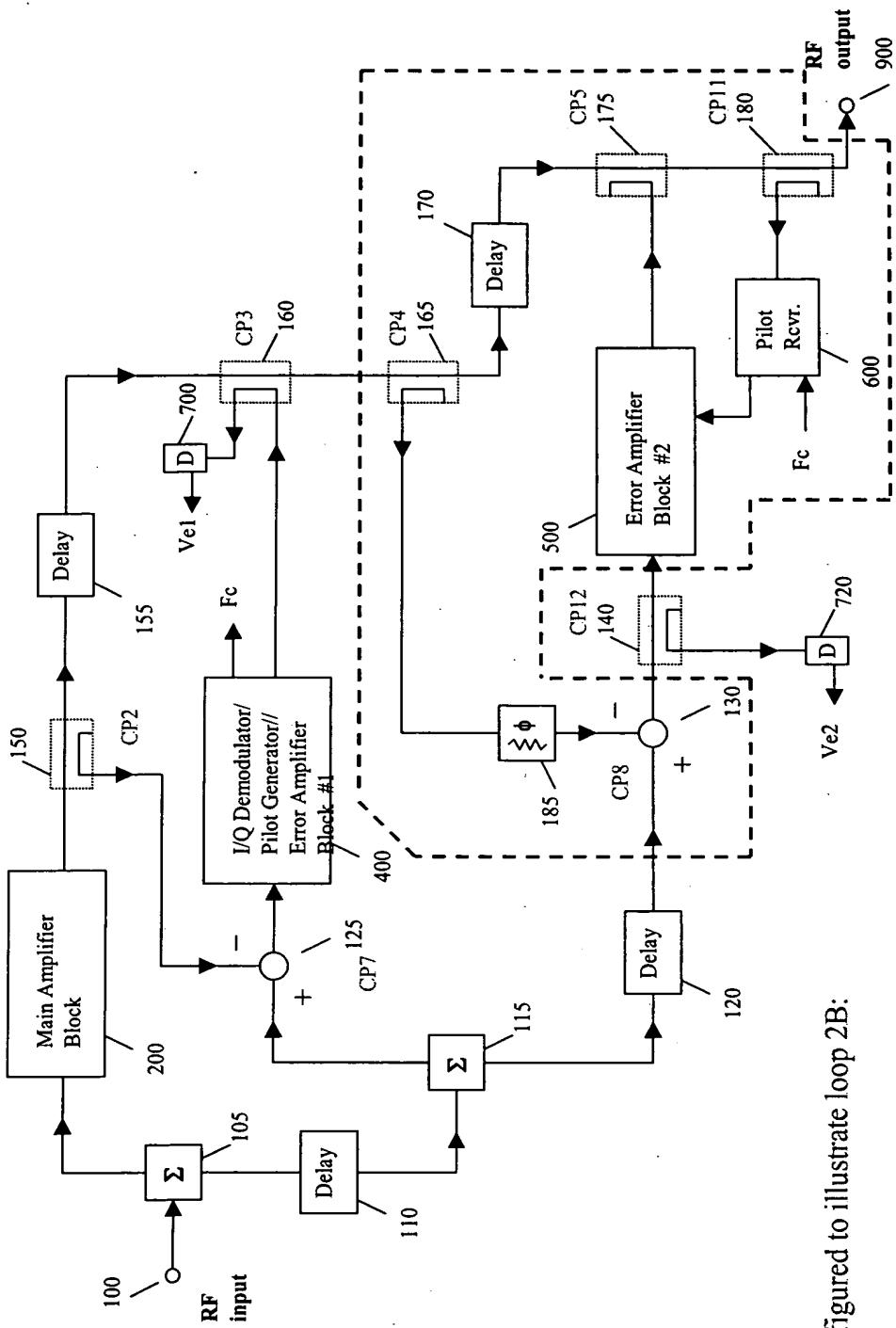


FIG. 8

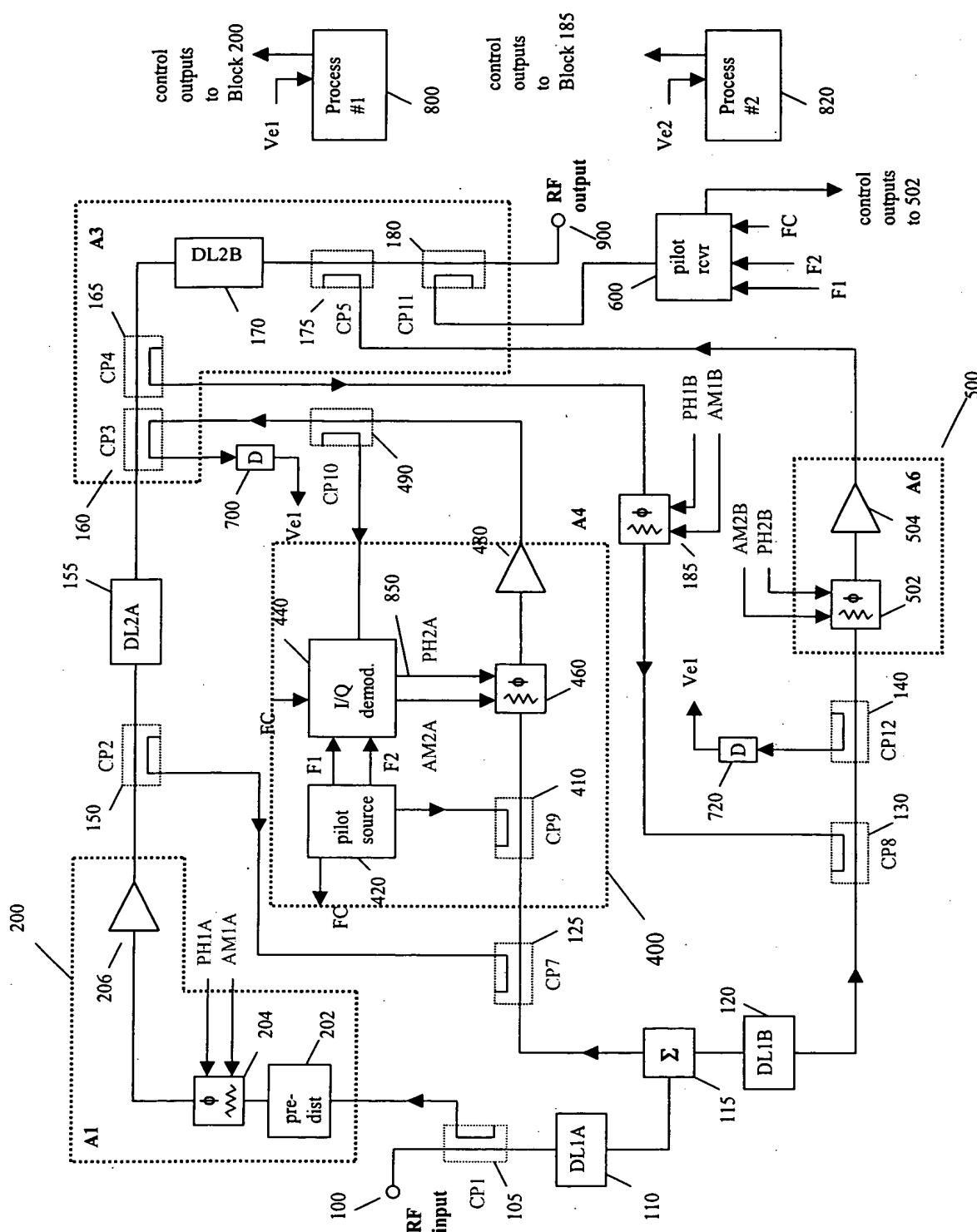


FIG. 9

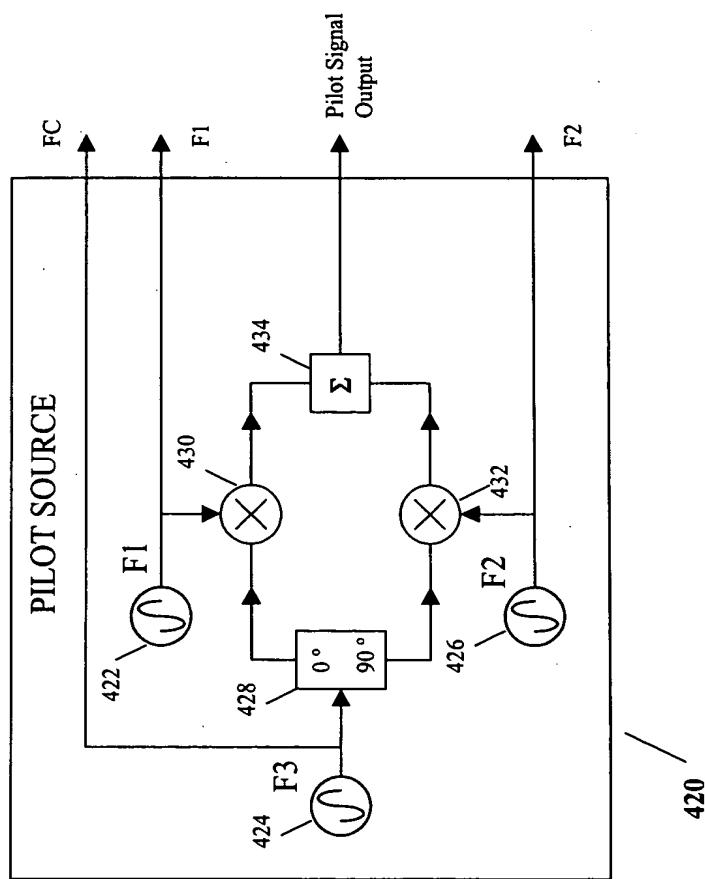


FIG. 10

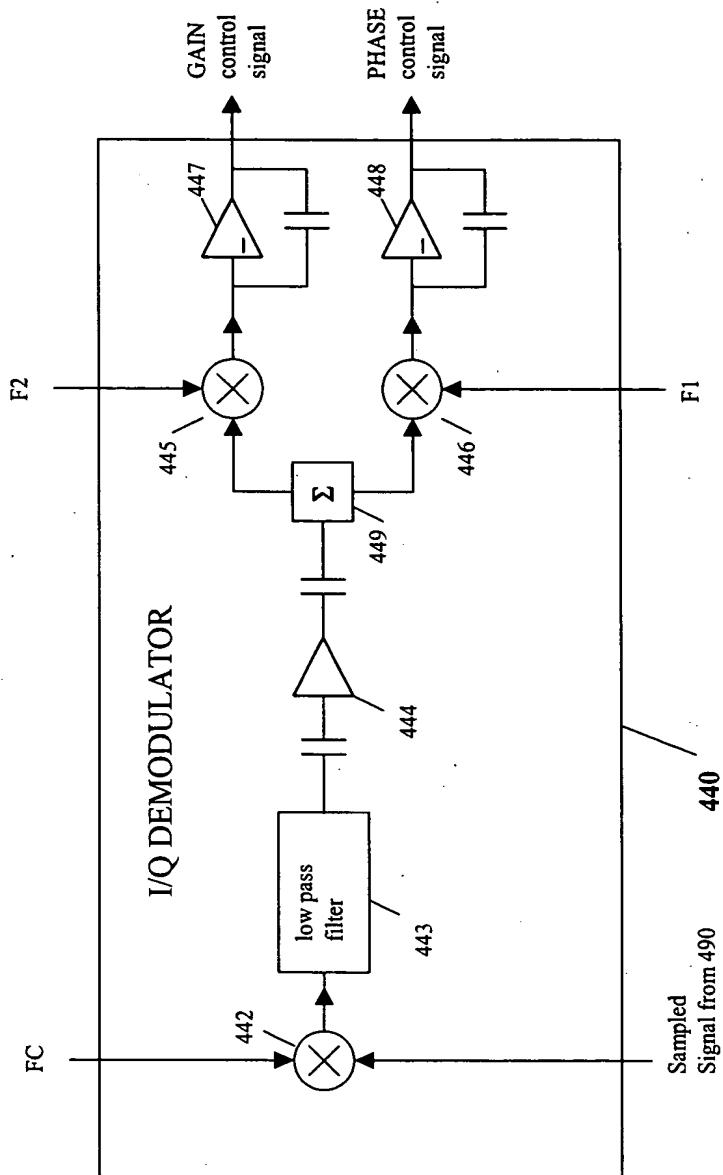


FIG. 11

Configured to illustrate output coupler loss minimization loop only:

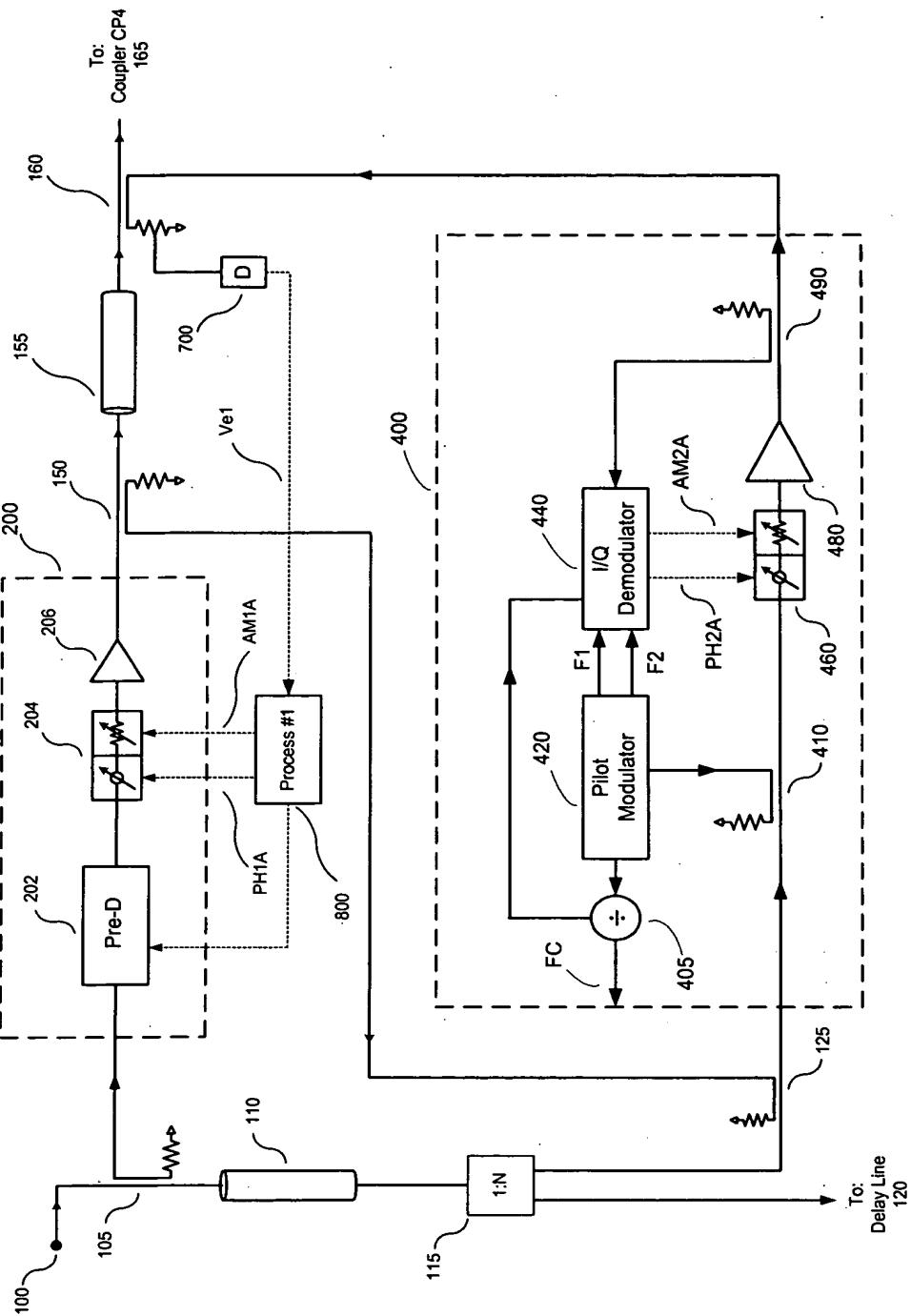


FIG. 12

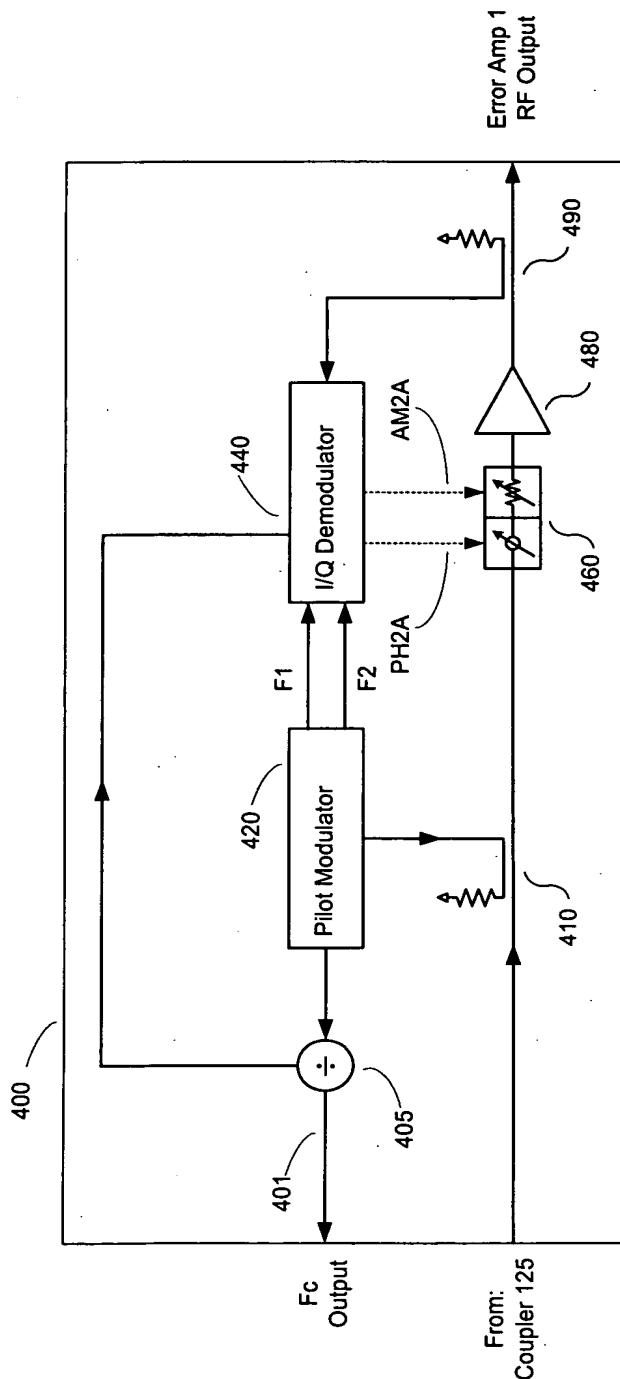


FIG. 13A

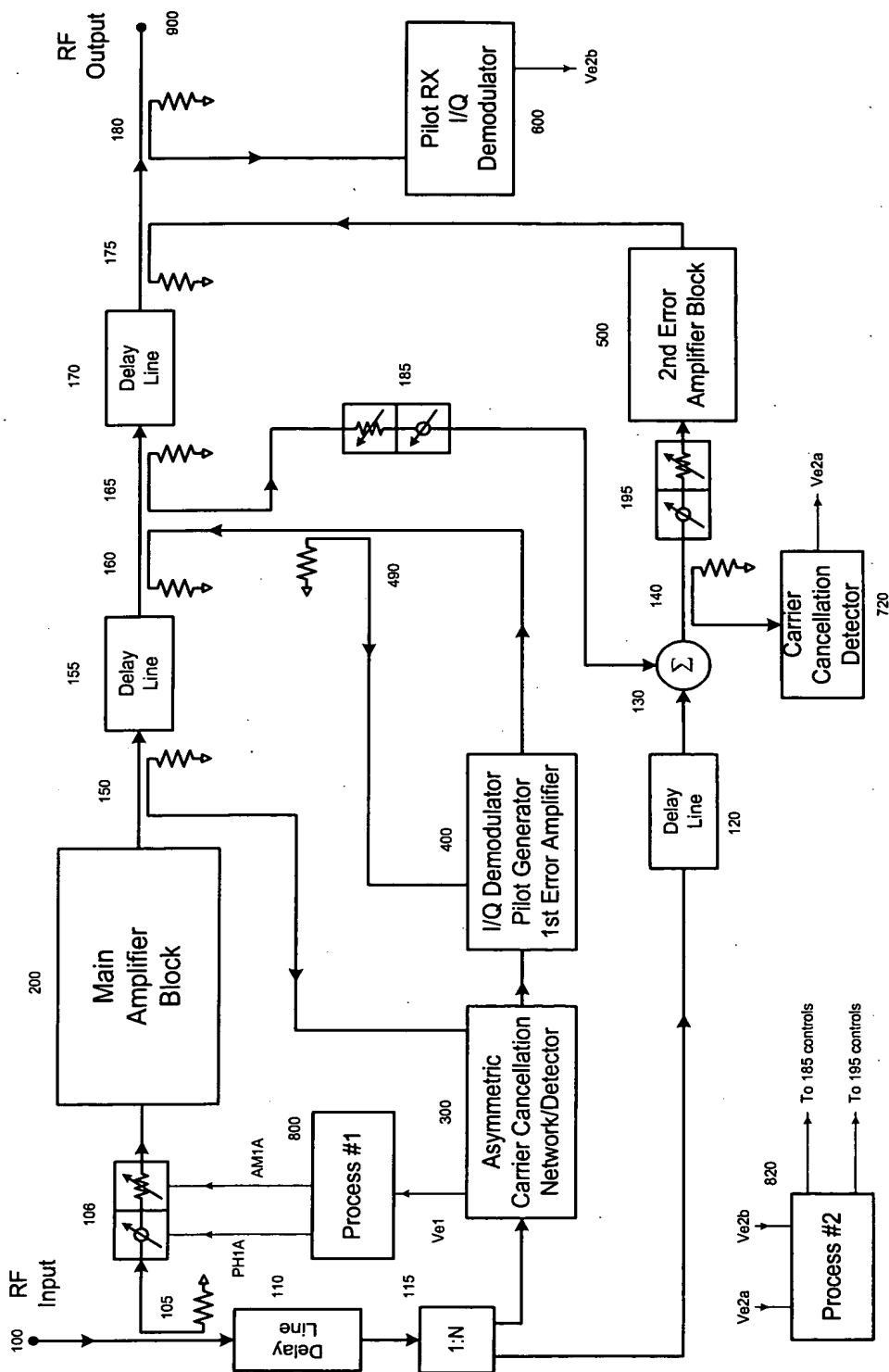
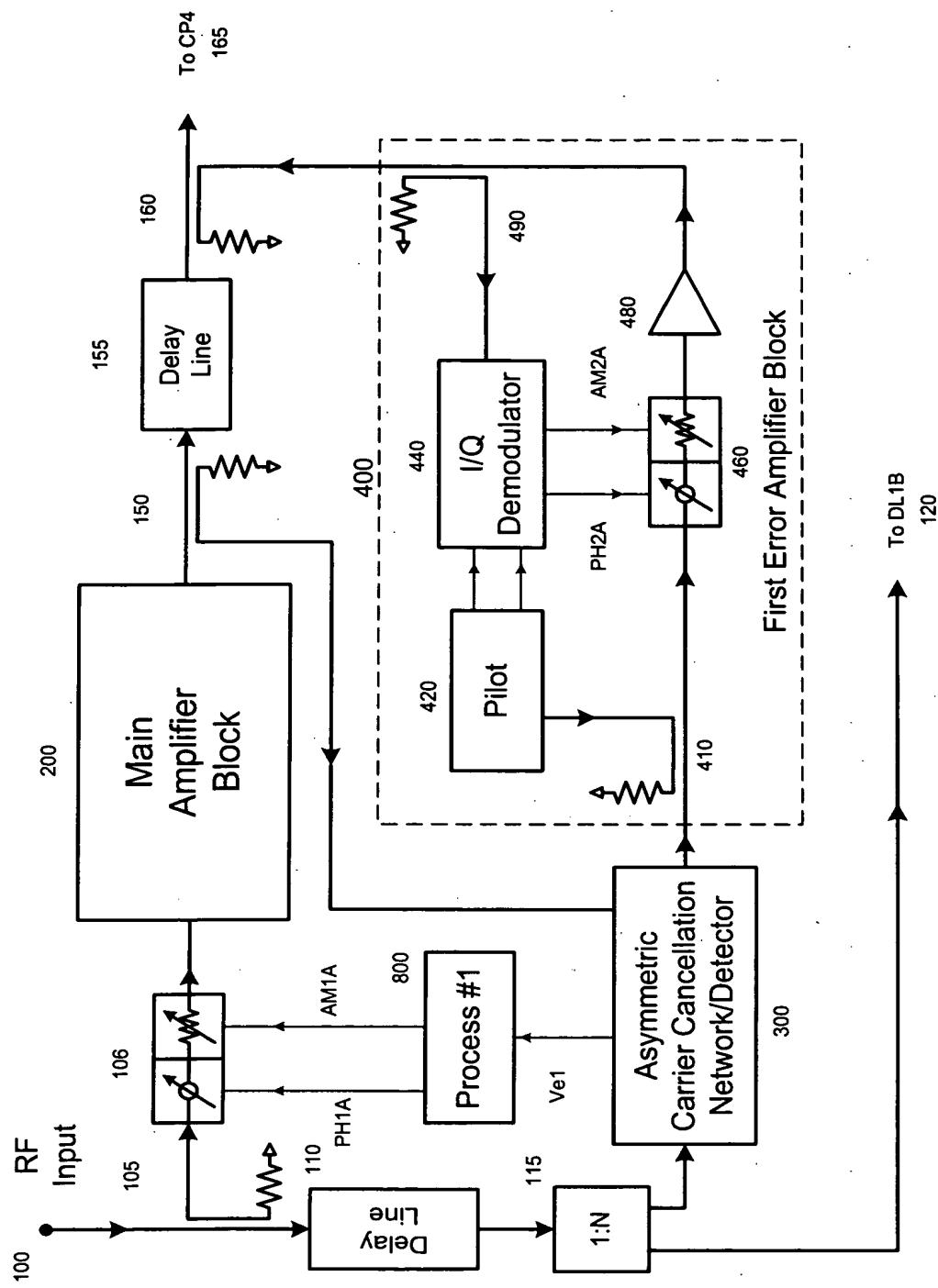


FIG. 13B



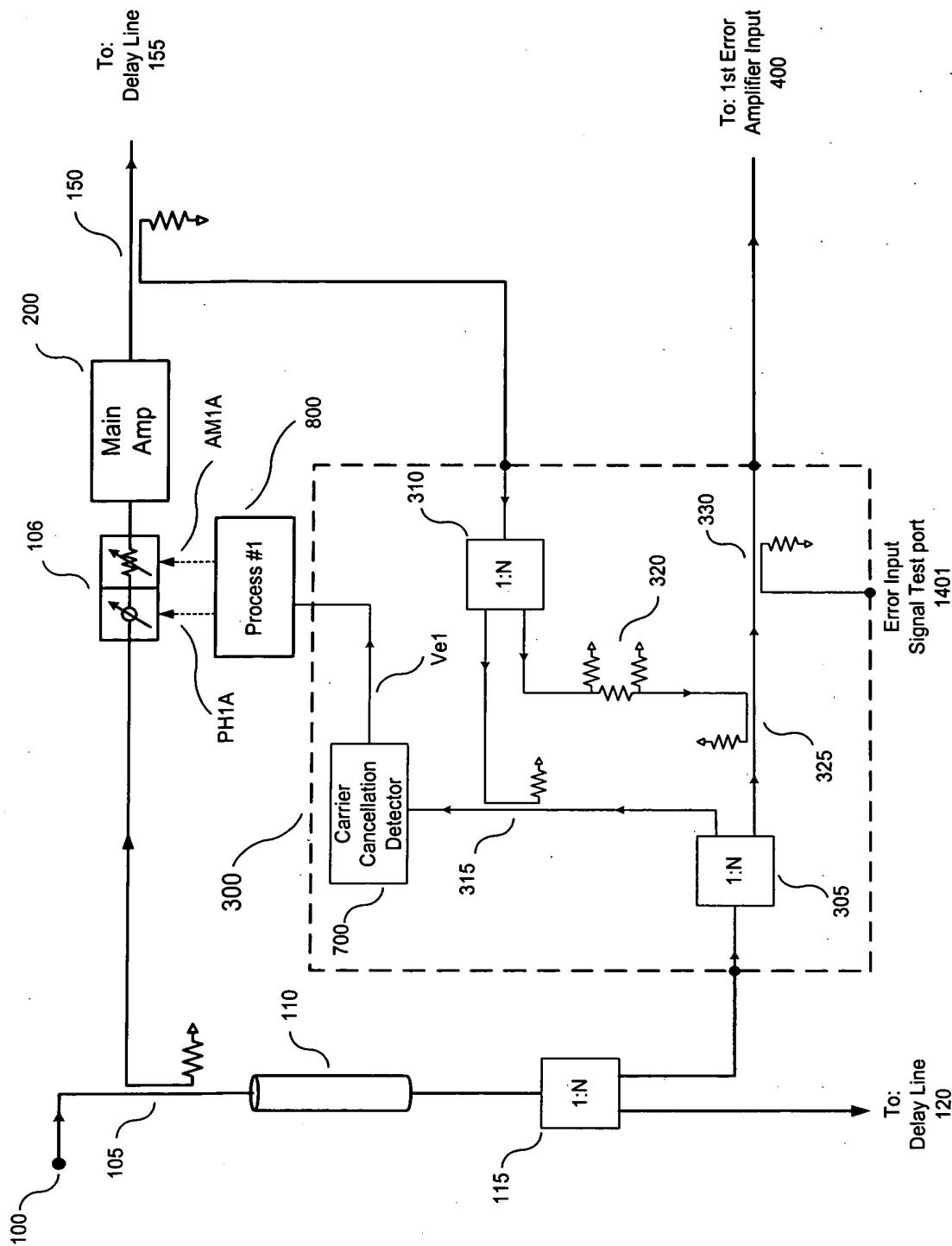


FIG. 14

1st Error Amplifier Stabilization

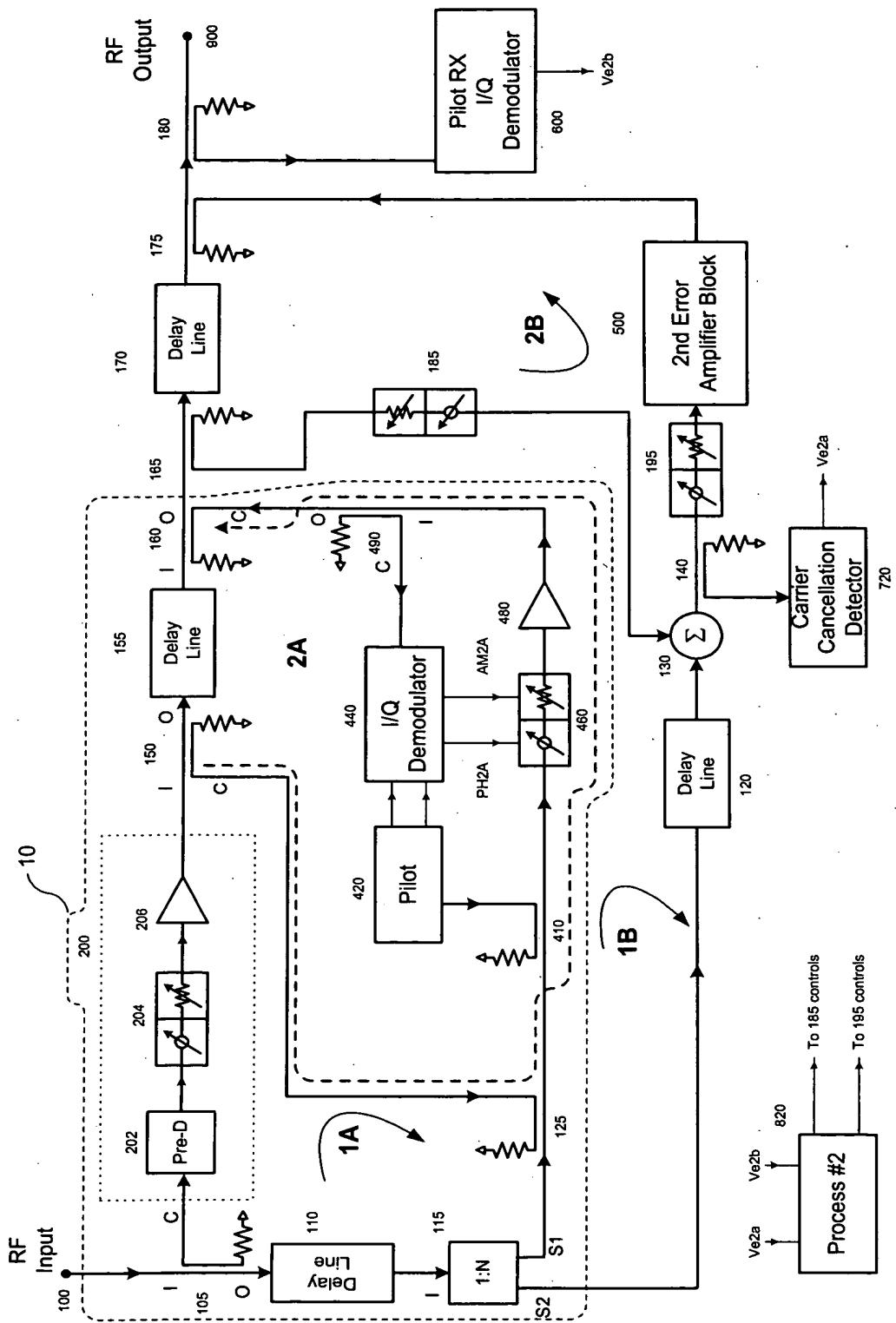


FIG. 15

Error Amplifier Stabilization

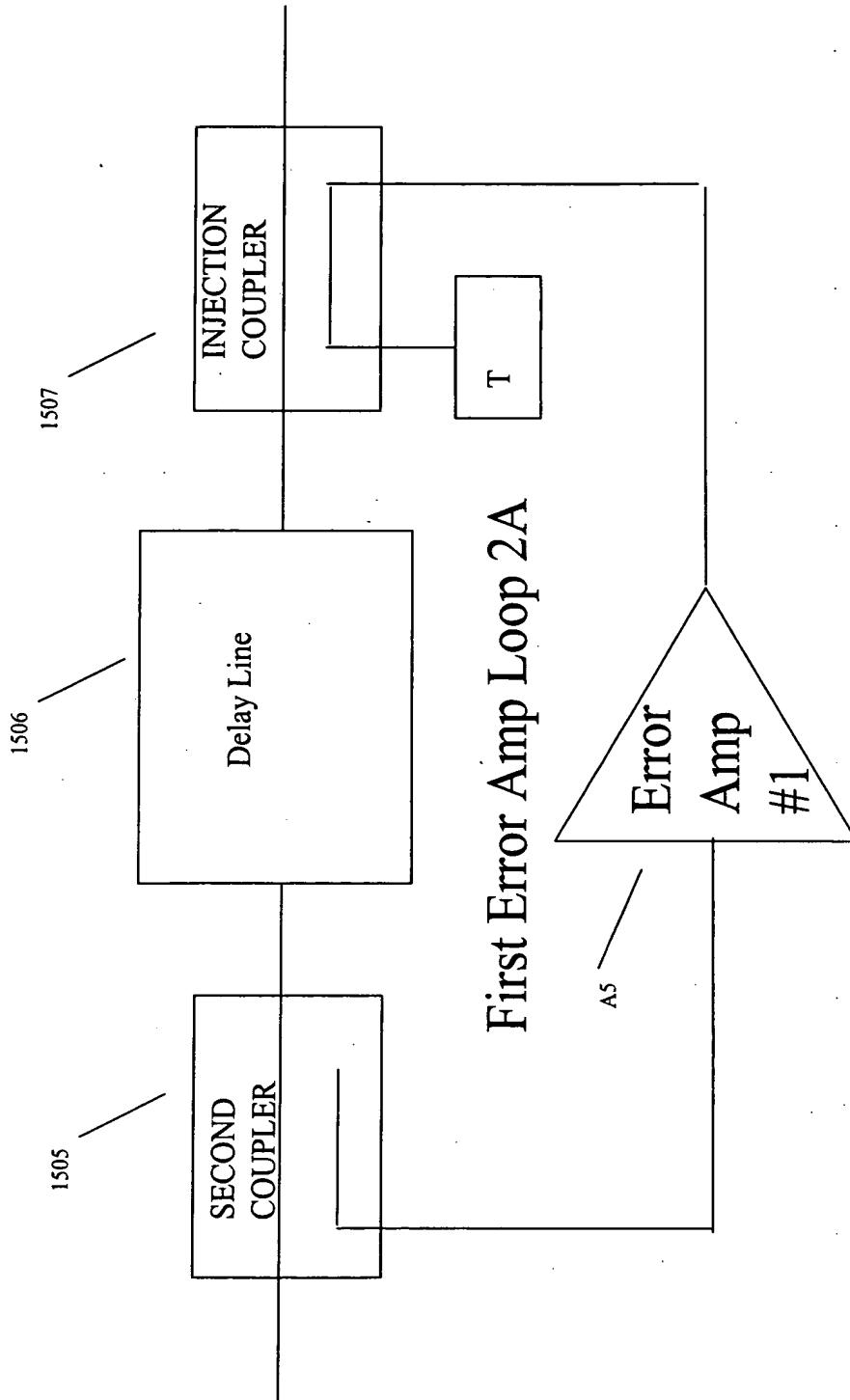


FIG. 16

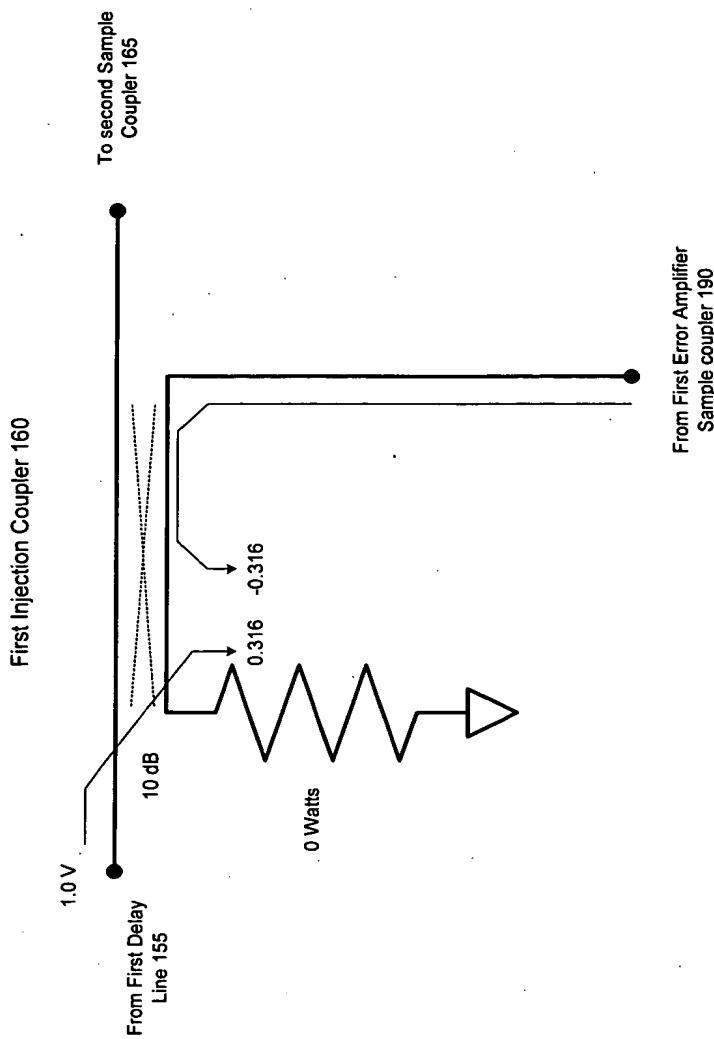


FIG. 17

Carrier Re-Injection

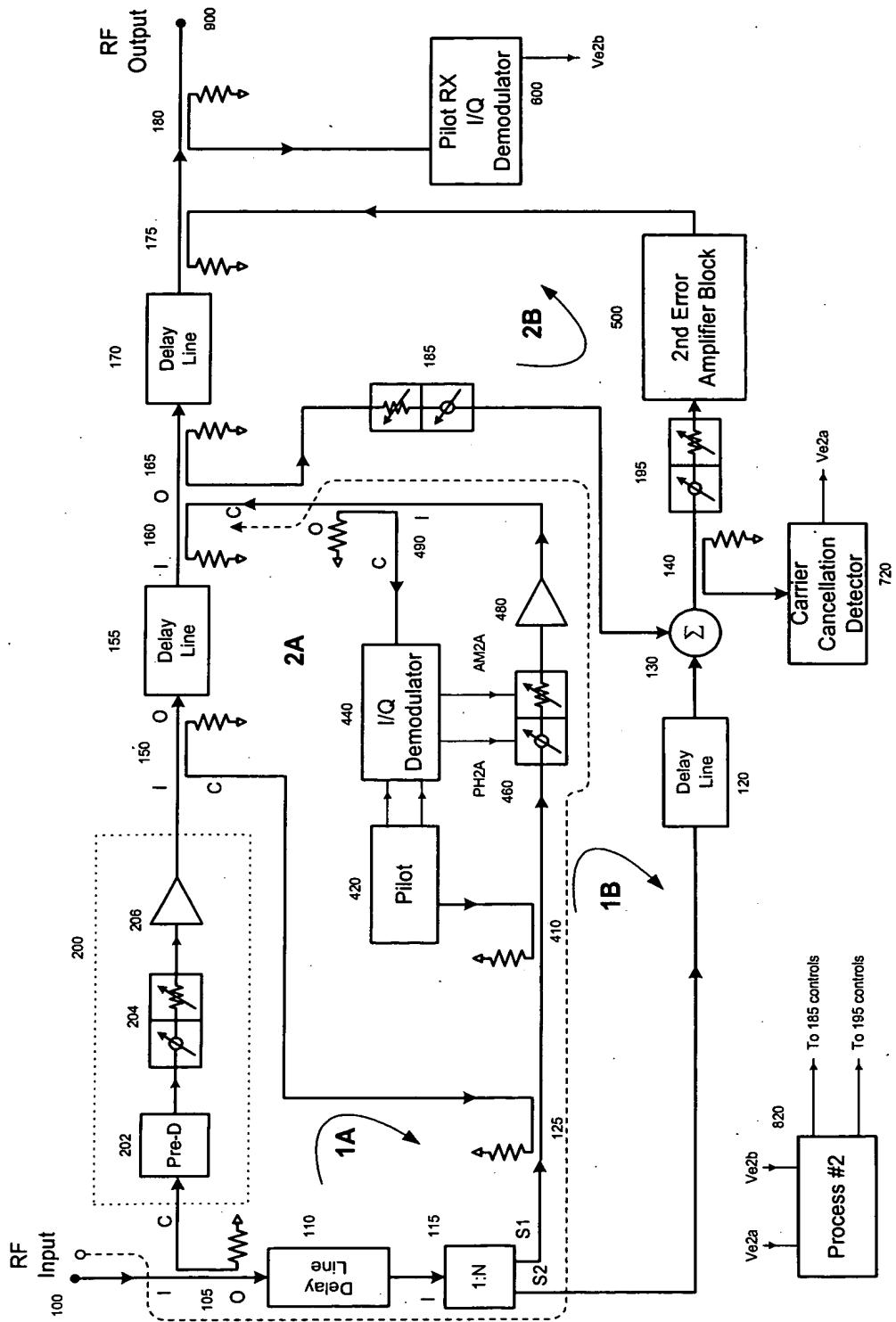


FIG. 18

Non-Overlapping Loop Adjustment

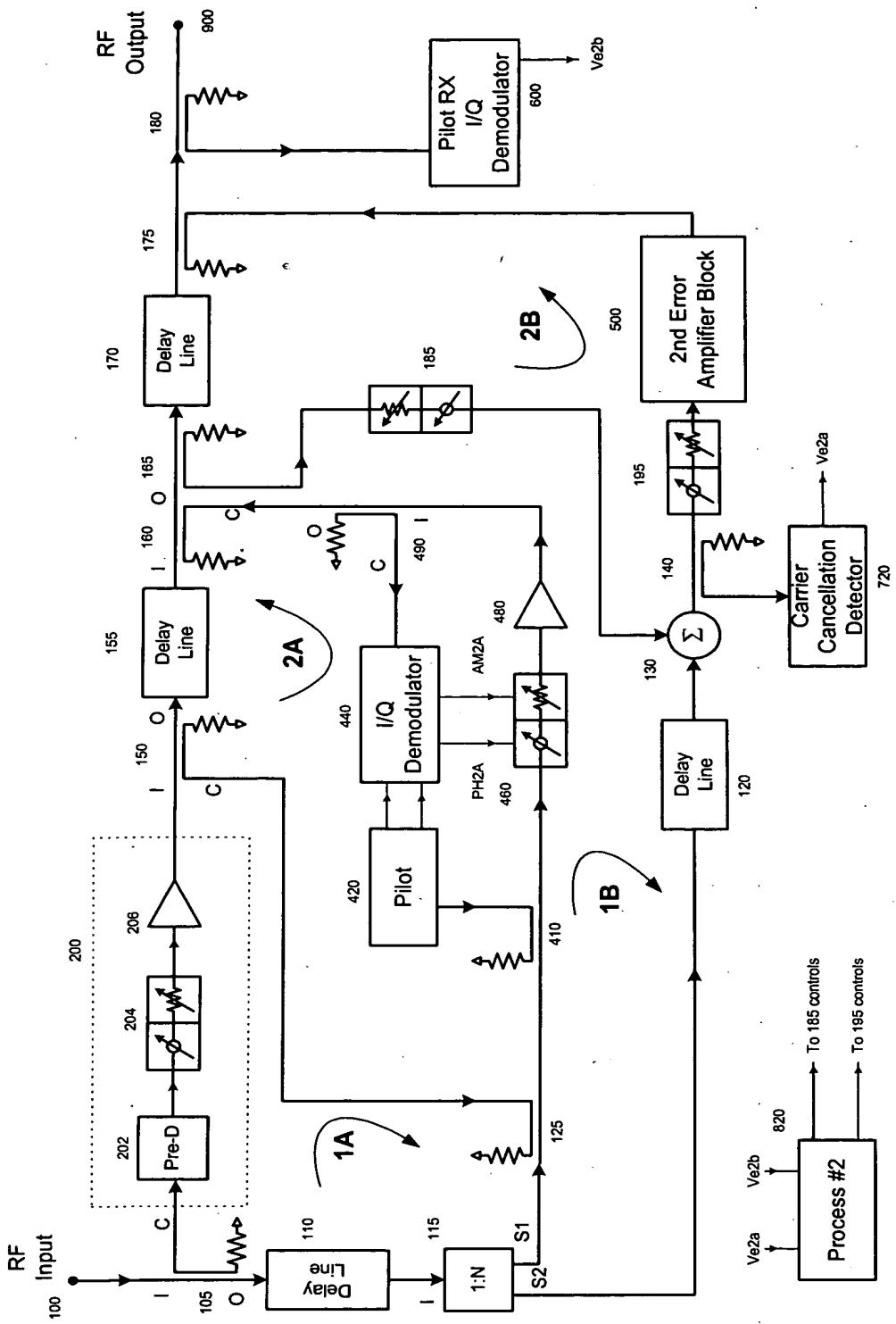


FIG. 19